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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2183 Jw

Application No.: 09/761,494 Filed: 01/16/2001 Inventor(s): Micah C. Knapp Poonacha P. Kongetira Marc E. Lamere Julie M. Staraitis A SUPERSCALER PROCESSOR HAVING **CONTENT ADDRESSABLE** MEMORY FOR **DETERMINING DEPENDENCIES**

Examiner: Pan, Daniel H.

Group/Art Unit: 2183
Atty. Dkt. No: 5681-86000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

B. Noël Kivlin

Printed Name

G-28-04

Date

RESPONSE TO OFFICE ACTION OF June 28, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 28, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.